

S.S. Lab Equipments

PGLA55

PATTERN GENERATOR - LOGIC ANALYZER

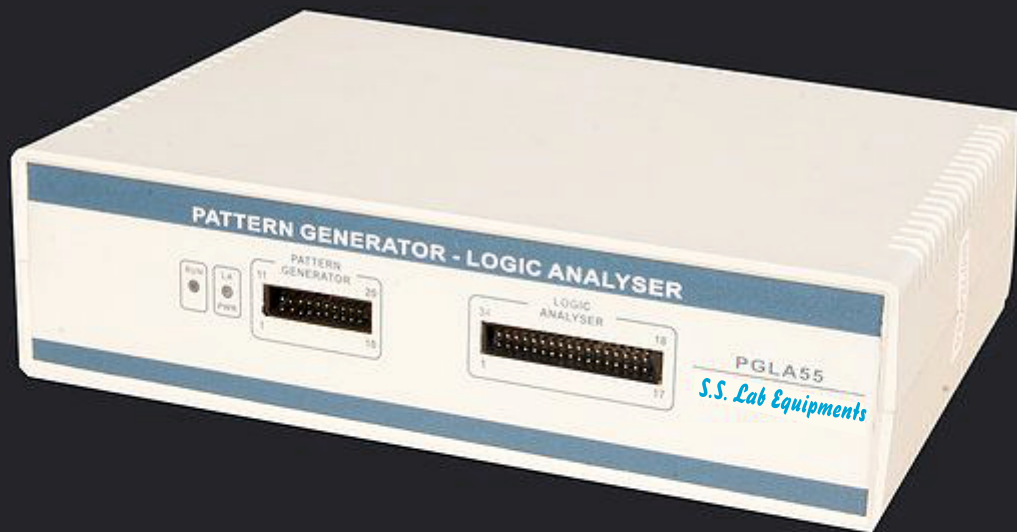
32 channel independent Input Logic Analysis at 200 M Sa/Sec

50MHz external clockstate analyzer
256ksampleshigh speed low voltage memory

High speed USB connectivity

Easy to use windows PC software

built in
I2C & SPI
protocol analyzer



16bit
independent
Pattern Generator
output

High speed pattern
generation
in terms of
Micro Sec/Nano sec.

Dedicated Trigger out
facility to connect
to external logic
Analyzer/Oscilloscope

About 256 Different patterns of 16 bit width can be downloaded and clocked from nano secs to secs

LOGIC ANALYZER SPECIFICATION

- 32 CHANNEL INDEPENDENT INPUT LOGIC ANALYSIS AT 200 M Sa/Sec
- 50MHZ EXTERNAL CLOCK STATE ANALYZER
- LOW POWER OPERATION
- INBUILT INPUT CAPTURE NOISE FILTERING
- 256K SAMPLES HIGH SPEED LOW VOLTAGE MEMORY
- EXTERNAL SAMPLING CLOCKS POSSIBLE
- AUTOMATIC SERIAL AND PARALLEL MODE TRIGGER
- USB CONNECTIVITY TO PC
- BUILT IN I2C AND SPI PROTOCOL ANALYZER
- EASY TO USE WINDOWS PC SOFTWARE

PATTERN GENERATOR SPECIFICATION

- 16 BIT INDEPENDENT PATTERN GENERATION OUTPUT
 - PATTERN MODE,COUNTER MODE AND SEQUENCE MODE OF OPERATIONS
 - TTL/CMOS COMPATIBLE OUTPUTS
 - HIGH SPEED PATTERN GENERATION IN TERMS OF MICRO SECONDS/NANO SECONDS
- ABOUT 65536 DIFFERENT PATTERNS OF 16 BIT WIDTH CAN BE DOWNLOADED AND CLOCKED FROM NANO SECS TO SECS
 - HIGH SPEED USB CONNECTIVITY
- DEDICATED TRIGGER OUT FACILITY TO CONNECT TO EXTERNAL LOGIC ANALYZER/OSCILLOSCOPE
 - LOW POWER OPERATION
- EASY TO USE WINDOWS PC SOFTWARE

LOGIC ANALYZER

- MAXIMUM DIGITAL SAMPLING RATE: 200MSPS
- NUMBER OF DIGITAL INPUT CHANNELS: 32
- BUFFER DEPTH: 256K
- TRIGGERMODES: AUTO, NORMAL
- INTERNAL CLOCKING: YES
- EXTERNAL CLOCKING: YES
- EXTERNAL TRIGGER: YES
- POST TRIGGER STORE: YES
- PRE TRIGGER STORE: YES
- DIGITAL NOISE FILTER: YES
- DIGITAL INPUT LOW : <0.8V
- DIGITAL INPUT HIGH : >2.0V
- DIGITAL INPUT MAX: +5V
- DIGITAL INPUT MIN: 0V
- TRIGGER AT LEVELS: YES
- TRIGGER MASK BITS: YES
- TRIGGER POSITION SETTING: 0-100
- INPUT IMPEDANCE: 1MOHM 30PF
- COMMUNICATION WITH PC: USB

PATTERN GENERATOR

- NUMBER OF DIGITAL OUTPUT CHANNELS: 16
 - DIGITAL OUTPUT MAX: +5V
 - DIGITAL OUTPUT MIN : 0.1V
 - FANOUT: 20 TTL
 - OUTPUT IMPEDANCE: 1K 10PF
 - AUTO REPEAT PATTERNS: YES
 - TRIGGER OUT ON RUN: YES
 - VISUAL RUN INDICATION: YES
- SELECTABLE PATTERN UPDATE RATE: YES
 - PATTERN CLOCK MINIMUM: 100NS
 - PATTERN OUTPUT TIME SELECT: YES
- CAN CONNECT DIRECTLY TO LOGIC ANALYZER: YES
 - COUNTER MODE: YES
- COMMUNICATION WITH PC:USB

POWER SUPPLY

USING SINGLE SMPS ADAPTER FOR BOTH LA AND PG

IO CONNECTION

1. 20 PIN FRC MALE RIGHT ANGLED CONNECTOR FOR PG
2. 34 PIN FRC MALE RIGHT ANGLED CONNECTOR FOR LA

DATA MODIFY / VIEW

PC BASED SOFTWARE FOR PG AND LA RUNS ON WINDOWS XP,VISTA AND WINDOWS 7

* SUBJECT TO CHANGE